

L Number	Hits	Search Text	DB	Time stamp
1	93	((fus\$2 adj3 block\$2) and (fus\$2 adj3 control\$4)) and (memory adj3 cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 17:58
2	73	((fus\$2 adj3 block\$1) and (fus\$3 adj3 control\$4)) and (memory adj3 cell\$1)) and 365/\$7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 17:59
3	38	((fus\$2 adj3 block\$1) and (fus\$3 adj3 control\$4)) and (memory adj3 cell\$1)) and 365/225.7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 17:59
4	176	((365/225.7.ccls. and fus\$2) and memory) and (fus\$2 adj3 (controller or control or controlling))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 17:59
5	52	(BISR or built\$lin\$1self\$1repair) and memory and fus\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:00
7	49	(BISR\$1 or built\$lin\$1self\$1repair) and 365/\$7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:01
8	62	(fus\$2 adj3 block\$1) with (fus\$3 adj3 control\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:01
9	1	(fus\$1 adj3 controller\$3) same memor\$3 same (fus\$2 adj3 block\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:03
10	5	(fus\$1 adj3 controller\$3) and memor\$3 and (fus\$2 adj3 block\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:03
11	3	((fus\$1 adj3 controller\$3) and memor\$3 and (fus\$2 adj3 block\$1)) and wafer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:06
12	0	(fus\$2 adj2 controller) and (fus\$2 adj2 block\$1) and BISR	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:08
13	0	((BISR\$1 or built\$lin\$1self\$1repair) and 365/\$7.ccls.) AND ((fus\$2 adj3 block\$1) with (fus\$3 adj3 control\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:08
14	1	((fus\$2 adj3 block\$2) and (fus\$2 adj3 control\$4)) and (memory adj3 cell\$1)) and ((BISR\$1 or built\$lin\$1self\$1repair) and 365/\$7.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:08

6	5	((BISR or built\$lin\$1self\$1repair) and memory and fus\$2) and (fus\$2 adj3 control\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 18:11
-	2270	fus\$2 adj3 block\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/20 14:05
-	191	(fus\$2 adj3 block\$1) and (fus\$2 adj3 control\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/20 16:31
-	82	((fus\$2 adj3 block\$1) and (fus\$2 adj3 control\$4)) and (memory adj3 cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 17:58
-	66	((fus\$2 adj3 block\$1) and (fus\$2 adj3 control\$4)) and (memory adj3 cell\$1)) and 365/\$7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 17:59
-	33	((fus\$2 adj3 block\$1) and (fus\$2 adj3 control\$4)) and (memory adj3 cell\$1)) and 365/225.7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 17:59
-	881	365/225.7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/20 15:40
-	840	365/225.7.ccls. and fus\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/20 14:29
-	817	(365/225.7.ccls. and fus\$2) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/20 14:29
-	167	((365/225.7.ccls. and fus\$2) and memory) and (fus\$2 adj3 (controller or control or controlling))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 17:59
-	138	((365/225.7.ccls. and fus\$2) and memory) and (fus\$2 adj3 (controller or control or controlling))) not (((fus\$2 adj3 block\$1) and (fus\$2 adj3 control\$4)) and (memory adj3 cell\$1)) and 365/\$7.ccls.) not (((fus\$2 adj3 block\$1) and (fus\$2 adj3 control\$4)) and (memory adj3 cell\$1)) and 365/225.7.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 18:00
-	127	BISR or built\$lin\$1self\$1repair	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/20 15:56
-	46	(BISR or built\$lin\$1self\$1repair) and memory and fus\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 18:00

-	5	((BISR or built\$lin\$self\$repair) and memory and fus\$2) and (fus\$2 adj3 control\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:00
-	45	(BISR or built\$lin\$self\$repair) and 365/\$7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:00
-	7	(BISR or built\$lin\$self\$repair) and 365/225.7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/02/20 16:31
-	2	((fus\$2 adj3 block\$1) with (fus\$2 adj3 control\$4)) with (memory adj3 cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/02/20 16:38
-	55	(fus\$2 adj3 block\$1) with (fus\$2 adj3 control\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/18 18:01
-	2	puri-mukesh.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/02/20 16:38
-	1	puri-mukesh-k.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/02/20 16:38
-	0	mukesh-puri.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/02/20 16:39
-	3	agrawal-ghasi.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/02/20 16:39
-	0	ghasi-agrawal.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/02/20 16:39